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A printed circuit board according to claim 15, wherein the alignment mark is used for positioning during mounting of a printed circuit board packaged as a semiconductor element to another printed circuit board.

41. A printed circuit board according to claim 16, wherein the alignment mark is used for positioning during mounting of a printed circuit board packaged as a semiconductor element to another printed circuit board.

42. A printed circuit board according to claim 17, wherein the alignment mark is used for positioning during mounting of a printed circuit board packaged as a semiconductor element to another printed circuit board.

43. A printed circuit board according to claim 18, wherein the alignment mark is used for positioning during mounting of a printed circuit board packaged as a semiconductor element to another printed circuit board. ---

REMARKS

The Examiner is respectfully requested to enter the foregoing amendment prior to examination of the above-identified patent application.

The present amendment adds claims that parallel claims which were amended by the June 11, 1999 Preliminary Amendment to eliminate multiple dependency. Accordingly, the amendments to the claims made in this amendment have not been made to overcome the prior art, and thus, should be considered to have been made for a purpose unrelated to patentability, and no estoppel should be deemed to attach thereto.

P17856.A03

Should there be any questions, the Examiner is invited to contact the undersigned at the below-listed telephone number.

Respectfully submitted,  
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